l	REV	EN	CHG		DESCRIPTI	ON		DR	ARBO
2	A	84179	-	PRO	DUCTION RELEASE			:/?	XX
98A 1169					,				
8		•		•			•		•
.	•			•					
DWG.						•			
	•				•	•			
							•		٠
1						•			
				•	·				
			•					•	
				•					
I				•				•	
				•					
			•.						
			CREAT SPECII HEREII J. F. System	FICATION MAY	ore Vice-	PLIED. WARACTE WITHOU	EQUIPMENT RISTICS STATED		
NEX	(T AS	SEMBL	Y		MODEL NO. 620f-118, 7X-9006		varian data machine 2722 michelson drive / irvi		
DR CHK	-+-	M. Jol	hnson	9/75	CODE 21101		ES I/O EXPANDER		
DSG	_			-	THIS DOCUMENT MAY CONTAIN PROPRIETARY INFORMATION AND	HAR	WARE PERFORMA	NCE SPE	:C.
ENG	R L.	Ξ. Ή.	ا محد ا	9-3-7	SUCH INFORMATION MAY NOT BE DISCLOSED TO OTHERS FOR ANY	SIZE	DWG NO.		REV
APPE		til=	1	9-8-75	PURPOSE OR USED TO PRODUCE THE ARTICLE OR SUBJECT, WITH-	A	98 A1169		A.
4000	1	2/1	-	1./	OUT PERMISSION FROM VDM				

REVISIONS

1.0 INTRODUCTION

The purpose of the DM 394 Series I/O Expander (44P0670) is to provide the capability of expanding the I/O bus of a 620 Series or V70 Series computer. The DM 394 provides connection for up to 10 additional peripheral device controllers, BICs, and/or PIMs. All input/output signals of the DM 394 have characteristics and timing similar to those of the I/O bus which is expanded.

The DM 394 is a printed circuit version of the earlier wire wrap DM 297 I/O expander. The DM 394 is implemented with faster IC types thus presenting less throughput delay and control delay. It requires only one card slot instead of three. In addition, it provides two priority look-ahead circuits which are not available on the DM 297. The DM 394 supersedes and replaces the DM 297 for all applications.

varian data machines

CODE 1DENT NO. **21101**

98A1169

sh 2 OF 17

A REV

2.0 FUNCTIONAL DESCRIPTION

A functional block diagram of the Series I/O Expander is shown in Figure 1. The numbers (Pn) within the blocks reference the applicable page of the logic diagram (91C0435).

2.1 Control Signal Drivers and Receivers

This section provides the receivers and drivers for receiving and repowering the unidirectional I/O bus control signals. Typical circuits for control signals in and for control signals out are shown in Figure 2. In addition to being retransmitted, many of the control signals are used as control inputs by the control section.

2.2 Control

The control section utilizes changes in the I/O bus control signals to determine which way the bidirectional I/O E-bus signals will be gated (i.e., towards the computer or away from it). These changes fire one-shots which either set or reset an input-output control flip-flop. The two outputs of the flip-flop are repowered by inverters to generate the following control signals for use by the E-bus receiver and gated driver section:

Data Transfer Out enables (DTOX1+ and DTOX2+) and Data Transfer In enables (DTIX1+ and DTIX2+).

In the quiescent state, output transfers are enabled. The following conditions switch the enable from output transfers to input transfers:

- A. Occurrence of the trailing edge of Function Ready (FRYX) during a Data-Transfer-In instruction.
- B. Occurrence of the leading edge of Interrupt Acknowledge (IUAX) during a Trap In (TPIX), Trap Out (TPOX), or Interrupt (IUAX).

The following conditions will switch the enable from input transfers to output transfers:

- A. Occurrence of System Reset (SYRT).
- B. Occurrence of the trailing edge of any Data Ready (DRYX).
- C. Occurrence of the trailing edge of Function Ready (FRYX) during a Trap Out (TPOX).
- D. Occurrence of the trailing edge of Interrupt Acknowledge (IUAX).

The control timing is summarized in the timing diagrams of Figure 3.

2.3 E-Bus Receivers and Gated Drivers

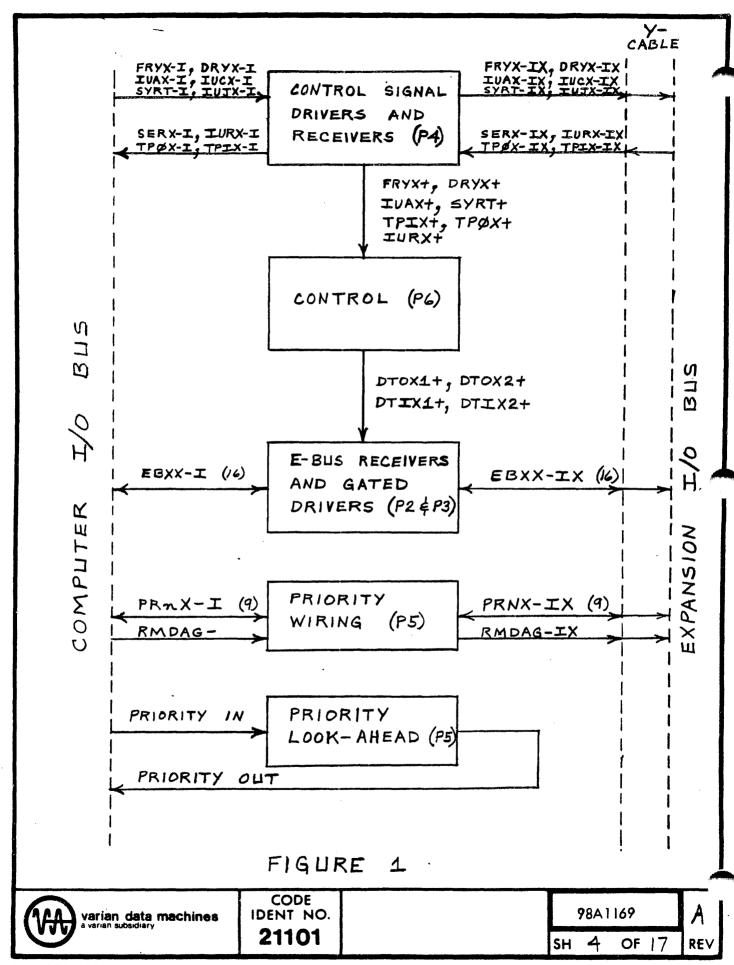
This section contains the receivers and gated drivers for receiving and repowering the bidirectional E-bus. A typical circuit for the E-bus line is shown in Figure 2. The drivers are gated by data-transfer-in enable (DTIX1+,2+) and data-transfer-out enable (DTOX1+,2+) signals from the control section. These signals control the direction of data flow through the board.

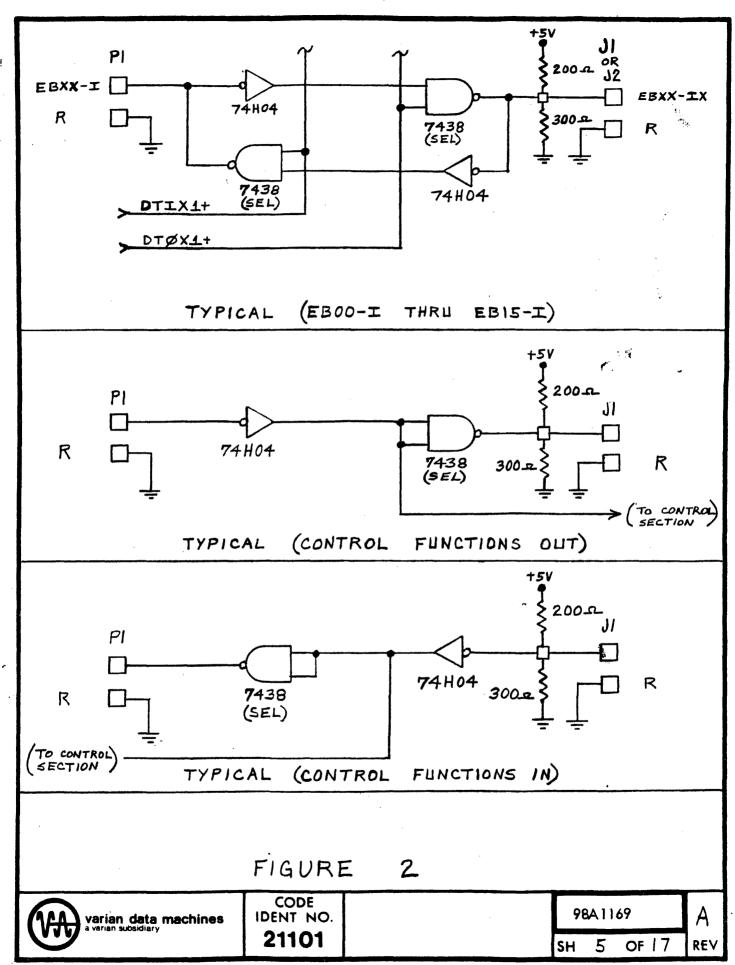


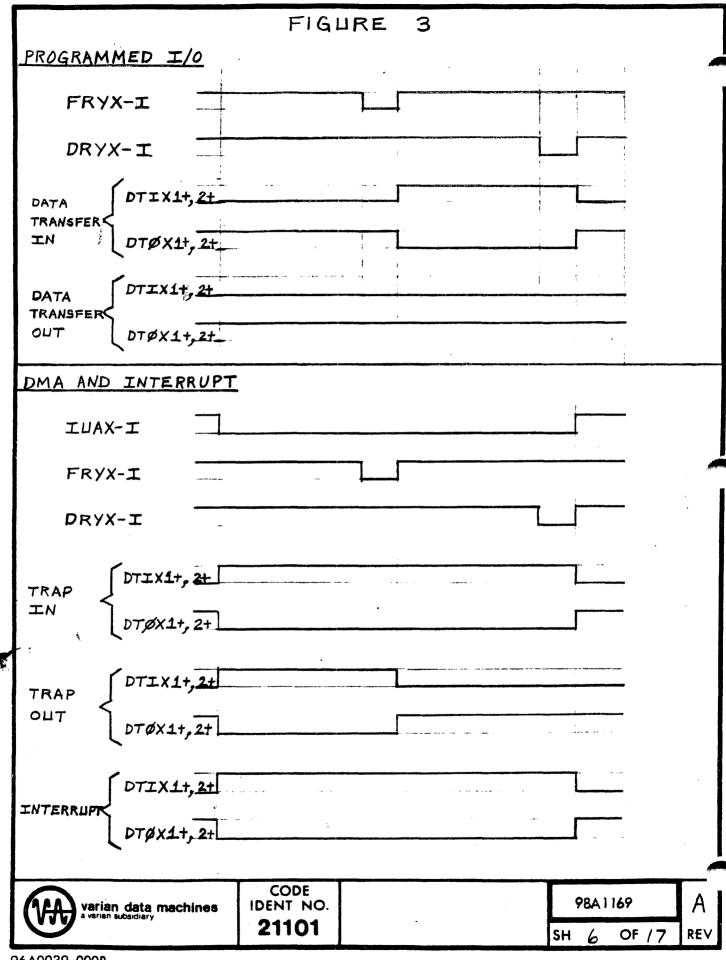
CODE IDENT NO. **21101**

98A1169

SH 3 OF 17







96A0039-000B

2.4 Priority Wiring

Four twisted pairs are provided on the board to bring priority lines through the board between the P1 connector of the board and the J1 connector (typical circuit shown in Figure 4). In addition, one twisted pair is provided between P1 and J2 to bring Rotating Memory Data Guard (RMDAG-) through the board. Provision is made for five more twisted pairs which can be added at the users option for bringing additional priority lines through the board (between P1 and J2).

2.5 Priority Look-Ahead

Two separate priority look-ahead circuits are provided for convenience in system configuration. A typical circuit is shown in Figure 4.

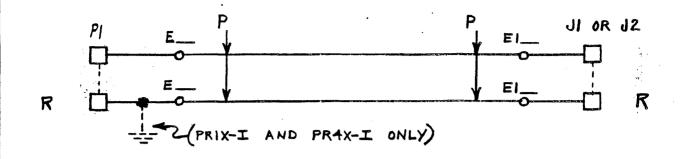
Each circuit receives its inputs and outputs via the P1 connector. However, uncommitted priority line pins on the J2 connector may alternatively be tied into the look-ahead circuits using the E-points provided on both the pins and the look-ahead circuits.

CODE IDENT NO. **21101**

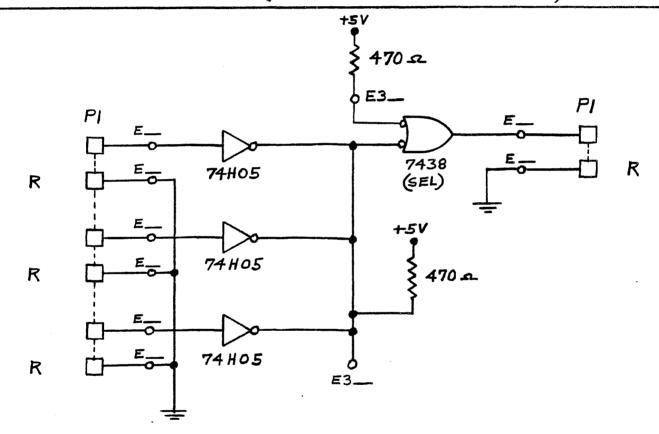
98A1169

7 OF 17

A.



TYPICAL (PRIX-I THROUGH PR9X-I, RMDAG-)



TYPICAL (PRIORITY LOOK-AHEAD)

FIGURE 4

varian data machines a varian subsidiary

varian subsidiary

CODE IDENT NO.
21101

98A1169

SH 8 OF /7 REV

3.0 PHYSICAL DESCRIPTION

The DM 394 Series I/O Expander is packaged on one 7 3/4-by-12-inch printed circuit board and requires only one I/O board slot. The Series I/O Expander Option (01P1095) also includes a term shoe and an expansion cable. The expansion cable attaches to the J1 and J2 connectors of the board and has a paddleboard connector at the opposite end for plugging into the expanded bus backplane.

CODE IDENT NO. 21101

98A 1169

OF 17

INTERFACE DATA 4.0 4.1 Logic Levels → +2.4 VDC to +3.2 VDC False VDC to +0.5 VDC True 4.2 Signal Definitions Description Function Mnemonic EB00-1 through Bi-directional I/O bus data lines used to transmit E-bus lines 00 through 17 EB17-1 data between the CPU and the 1/O controllers. I/O bus control line from the computer used to FRYX-I Function ready indicate to the external device controller that the computer has placed a device address and a control code on the E-bus. Also, used during a DMA and/or interrupt to indicate that the computer has read the address from the E-bus. DRYX-I I/O bus control line from the computer used to Data ready indicate to the external device controllers that the data transfer has been completed during a data transfer instruction or DMA sequence. IUAX-I I/O bus control line from the computer used to Interrupt acknowledge acknowledge to the requesting external device controller that a DMA or interrupt operation is in progress. IUCX-I Interrupt clock I/O bus clock line used to synchronize DMA and interrupt operation on the I/O bus. IUJX-I Interrupt jump I/O bus control line used to inhibit all interrupting device controllers on the I/O bus when a jump-andmark instruction occurs during an interrupt sequence. IURX-I Interrupt request I/O bus control line from a peripheral controller or PIM requesting the computer to initiate an interrupt cycle. PRnX-I I/O bus priority line n where n = 1, 2, ... 9. Priority line n SYRT-I System reset I/O bus control line from the computer used to initialize external device controllers on the I/O bus

(continued)



CODE IDENT NO. **21101**

98A1169

SH 10 OF 17

Mnemonic	Description	<u>Function</u>
SERX-I	Sense response	I/O bus control line used to transmit the status of a selected external device condition from the selected external device controller to the computer.
TPIX-I	Trap in request ,	I/O bus control line from a peripheral controller or BIC requesting the computer to initiate a trap (DMA) in cycle.
TPOX-I	Trap out request	I/O bus control line from a peripheral controller or BIC requesting the computer to initiate a trap (DMA) out cycle.

NOTE:

Mnemonics for corresponding signals on the expansion I/O bus end

in "-IX" instead of "-I".

4.3 **Terminations**

The terminations for one end of the expansion I/O bus are located on the DM 394 board. A term shoe is supplied with the Series I/O Expander option to provide the terminations for the other end of the expansion I/O bus.

COL)E
IDENT	NO.
211	01

98A 1169

SH / | OF |

4.4 Pin Assignments

4.4.1 Main I/O Bus

Name	Connector - Pin
GRD	P1- 1
EB00-1	- 2
R	- 3
EB01-I	- 4
R	- 5
EB02-I	- 6
R	- 7
EB03-1	- 8
R	- 9
EB04-1	-10
EB05-I	-11
EB06-1	-12
EB07-I	-13
EB08-1	-14
EB09-1	-15
EB10-I	-16 17
EB11-I	-17
EB12-I	-18
EB13-I EB14-I	-19 -20
EB15-I	-20 -21
R	-21 -22
R	-24
R	-2 4 -26
FRYX-I	- 27
DRYX-I	- 2 9
R	-30
SERX-I	-31
R	-32
TPIX-I	- 33
R	- 34
TPOX-I	- 35
R	-36
PR1X-I	- 37
R	- 38
PR2X-I	- 39
R	-40
PR3X-I	-41
PR4X-I	-42
SYRT-I	-4 3
(continued)	



CODE IDENT NO. 21101

98A 1169

SH /2 OF /7

Name	Connector - Pin
IUAX-I	P1-44
IUCX-I	-45
IURX-I	-46
IUJX-I	-47
R	- 51
R	-53
R	-59
PR5X-I	-60
PR6X-I	- 63
R	-64
PR7X-I	-6 5
R	-66
PR8X-I	-67
R	-6 8
PR9X-I	- 69
R	-7 0
RMDA G-	-71
R	-7 2
LA1A-	-80
R	-81
LA1B-	-82
R	-83
LA10-	-84
R	-85
LA2B-	-86
R .	-87
LA2C-	-88
R	-89
(Ret) LA2O-	-90
R	-91
R	-105
LA1C-	-106
R	-109
LA2A-	-110 -118
+5V +5V	
GRD	-121 -122
GKD	-122

varian data machines	3
----------------------	---

COI	Œ
IDENT	NO.
211	01

98A	11	69

4.4.2 Expanded I/O Bus

Name	Connector - Pin
EB13-IX	J1- 2
R	- 3
EB14-IX	- 4
R	= 5
EB15-IX	- 6
R	- 7
FRYX-IX	- 8
R	- 9
DRYX-IX	-10
R	-11
SERX-IX	-12
R	-13
TPIX-IX	-14
R	-15
TPOX-IX	-16
R	-17
IUJX-IX	-18
R	-19
SYRT-IX	-20
R	-21
IUAX-IX	-22
R	-23
IUCX-IX	-24
R	- 25
IURX-IX	-26
R	-27
PR1X-IX	-3 0
R	-31
PR2X-IX	-32
R	-3 3
PR3X-IX	- 34
R	- 35
PR4X-IX	-3 6
R	- 37
EBOO-IX	J2- 2
R	- 3
EBO1-IX	- 4
R	- 5
EB02-IX	- 6
R	- 7
EBO3-IX	- 8
R	- 9
(continued)	



varian data machines

CODE IDENT NO. **21101**

98A 1169

SH 14 OF 17

Name	Connector - Pin
EB04-IX	J2-10
R	-11
EBO5-IX	-12
R	-13
EBO6-IX	-14
R	-15
EB07-IX	-16
R	-17
EBO8-IX	-18
R	-19
EB09-IX	-20
R	-21
EB10-IX	-22
R ·	- 23
EB11-IX	-24
EB 12-1X	-26
R	-27
R	-29
PR6X-IX	-30
R	-31
PR5X-IX	-32
R PR7X-IX	-33
	-34
R	-35
PR8X-IX	-36 -37
R	-37
PR9X-IX	-40
R	-41
RMDA G-IX	-42
R	-4 3

	CODE
٠	IDENT NO.
	21101

6.84

5.0 FEATURES AND OPTIONS

- 5.1 When more than one DM 394 Series I/O Expander is required in a system, the expanders must be placed in parallel on the computer I/O bus (not in series).
- 5.2 Each DM 394 presents one load to the computer I/O bus. The computer I/O bus can drive 10 loads maximum.
- 5.3 Each DM 394 can drive up to 10 loads maximum.

CODE IDENT NO. **21101**

98A 1169

SH 16 OF 17

6.0 APPENDICES

6.1 Reference Documents

01P1095, 01A1095 Parts list and top assembly, I/O Expander Option.

44P0670, 44D0670 Parts list and assembly, Series I/O Expander Board

40D0606 PW board, Series I/O Expander

97E0835 Artwork, Series I/O Expander

97E0836 Soldermask, Series I/O Expander

91C0435 Logic diagram, Series I/O Expander

·

5	3PE	ारर	Y ∳ UNI\				PARTS LIST	'l language de language de la language de la language de language	v
1	Ta E		SPE HHY	DAN/	AC IS	A Dn	VISION OF SPERMY HAND CO	CI WA	_
	1.40		XPALDLR	11	بلاء	1:L		A I A POLIT TORY	1
	IND N		CHARGE M CORED		U, M		DISTRIBUTION DE		-
ŀ				r		1	DIX UMENT NO DA	24	
	707			i			H-8796A -	02 PL HEV G, PIC HEV G, RANGE NO - 03 EIR HELEASED - 02/04/80	
1	700	!					W-87474 -	27 PL NEV F, PIC NEV D, RANGE OD - 03 EIK RELEABED 10/15/79	
		10.0		e	• • •	••		**************************************	
	1		,		EA		# 440:670 -	DO PC ASSEMBLY - SERIES I/O EXPANDER	4
١	7		AR		11		w 53004531 -	50 WIHE, STR, THISTED PATR, I.P. V.C. 30 AND BLACK & GREEN	
-	8		AR		F.A			na nastua sayan un	
ı	FOI	:			x			AND LODGE BANGLAN AND PARTIES OF THE	<u> </u>
- 1	501	•				١	î	ON PANT IDENTIFICATION MARKING SPEC	
- 1	20 <i>3</i>	: 1		!			!		
1		i			X		i	UN TEST SPECIFICATION I/O EXPANDER OPTION	^
	503			!	^		i		4
ŀ			• • • • • •		• • •	••	1	•• SEF TABULATION ON DRAWING • VARIABLE DATA • 90-4-44-4-4-4-4-4-4	• •
7	5		1		E A		H 5300537 -	OF CARLE ASSY, 1/U EXPANDER	4
ŀ		•	• • • • • •	•	• • •	• •		** SEE TAHIILATIUN UN DRAHING * VARIABLE DATA * 01***************	• 🕴
1	5		1		ξA		H 5300650 -	BO CABLE ASSY. I/O EXPANDER	A
ŀ	4 • •	• •		14.4		• •	* F3036-Un *	* SEE TABLILATION ON DRAWING * VARIABLE DATA OZageneganeganagan	•
	4		•		FA		6600709 -	AN CARLE ASSY - 1/0 BUS EXPANDER	A 4"
	S		1		EA		H 4400664 -	DE PC ASSEMPLY - TERM SHOE DHEGT NORMAL I/O AND HIGH SPEED DWA	A
L	4 6 6						3030-01	AND SET TARLILATION ON DRAWING . VARIABLE DATA . OBSESSABLES AREA	
Γ	4		1		E۵		i	THE CANAL THE STATE OF THE STAT	
	Ī	!			EA		i	OU TERMINATOR OPTION 1 TERMINATOR	
	b		,				4 (1) (1758)	to the training of the trainin	^
1	1						į		
							į		
	!	!					į		
١		;					i i	İ	1
1	1								1
								1	1
L	UDI I	1	ILV 2/25	<u>:</u> :	<u>. </u>	ل ـــا	IPI TCATE	SHEET 1 OF 1	

		revisions		
REV	EIR	DESCRIPTIONS	DR	APPD
D	W87474-27	RELEASED TO API	T.N	12.25.8 CMP
G	W87968-02	UPDATED FORMAT, DOC NO. WAS 0141095	T.N	12-25-8

TABULATION BLOCK							
PART NO.	FEATURE NO.	USED WITH					
W Q101095 - 00		620 F					
W 0101095 - 01		620 F WITH EXP CHASSIS 3					
W 0101095 - 02	F3036 - 00	620/L,F-100,V70 I/O					
W 0101095 - 03	F3036 - 01	V77, I/O EXP CHASSIS					

PART NO.: SEE TABULATION

FOR MATL REQUIREMENTS SEE PL PL REV LETTER CONTROLS DOCUMENT.

NEXT .	ASSEMBLY	,	MODEL NO.	SPERRY LUNIVAC			
DR	J.R.LUTHER	11-30-71	CODE 21101	TITLE	- · · · · · · · · · · · · · · · · · · ·		
СНК	DUSTON	2-21-72	THIS DOCUMENT MAY CONTAIN	1/0	EXPANDER OPTION	OPTION	
DSGN	J.R LUTHER	11-30-71		,			
ENGR	E.MC COY	5-22-72	DIRECT TO OTHER COR ANY	SIZE		REV	
APPD	P.W. ANDERSON	5-22-72	THE ARTIOLS OF OUR ISOT WITH	A	W 0101095	G	
APPD			UNIVAC	SHEET 1 OF			

NOTES: LINLESS OTHERWISE SPECIFIED

- 1. THE PURPOSE OF THIS OPTION IS TO PROVIDE CONNECTION FOR 10 ADDITIONAL PERIPHERAL DEVICE CONTROLLERS VIA THE CPU I/O CHANNEL ATHO!
- 2. IF OPTION IS TO BE SHIPPED FOR CUSTOMER INSTALLATION PACK IN SUITABLE CONTAINER AND MARK CONTAINER WITH THE FOLLOWING INFORMATION: I/O EXPANSION OPTION MODEL NO. (APPLICABLE MODEL AND SERIAL NO) PART NO. WOIO1095 - (APPLICABLE DASH NO. AND REVISION LTR)
- 3. FOR TYPICALINSTALLATION INTO A 620/f SEE FIGURES LOR 2
- 4. FOR TYPICAL INSTALLATION OF THE 02 VERSION SEE FIGURE 3.
- 5. FOR TYPICAL INSTALLATION INTO A V70 SERIES SYSTEM SEE FIGURES 4 OR 5. SEE SYSTEMS MEMO FOR DETAIL.
- 6. ADD PRIORITY WIRING TO P.C. VERSION (WAAOOG70) AT THE SYSTEMS LEVEL AS REQUIRED (REF W9100435 SHT 5). USE 30 AWG TWISTED PAIR GREEN/BLACK WIRE (S.U P/N W 5300453- 50) ROUTE WIRES PARALLEL TO EDGES OF BOARD AND SECURE IN PLACE WITH ADJUSTIVE (S.U P/N W 9000007 - 00).
- 7. INSTALL FIND NO 5 IN I/O EXPANSION CHASSIS SLOT IF AND AS SPECIFIED BY SYSTEMS MEMO.
- 8. FOR TYPICAL INSTALLATION OF 03 VERSION INTO A V77 I/O EXP CHASSIS SEE FIGURE G. REFER TO SYSTEMS MEMO FOR DETAIL.

CODE IDENT NO. 21101

W0101095

SH 2 OF 7

(NOTE: CONTINUED)

- 9. IN ALL CASES F/N I MUST BE INSTALLED ON THE CPN PRIMARY I/O BUS . I/O EXPANDERS MAY NOT BE INSTALLED ON AN EXPANDED BUS
- 10. CERTAIN SYSTEM CONFIGURATIONS MAY REQUIRE THE USE OF ADDITIONAL ALTERNATIVES OR MODIFIED ITEMS SUCH AS TERM. SHOE GUARDS, SPECIAL LENGTH CABLES ECT. REFER TO SYSTEMS MEMO FOR DETAIL. TYPICALLY, ITEMS/MATERIAL NOT USED IN THE FINAL CONFIGURATION IS TO BE RETAINED AT MCO.
- 11. ADDITION OF "LOOK AHEAD" CIRCUITRY. CERTAIN SYSTEMS REQUIRE THE IMPLEMENTATION OF PRIORITY "LOOK AHEAD". THIS WILL BE SPECIFIED VIA THE SYSTEMS MEMO AND WILL TYPICALLY INVOLVE MODIFICATION TO F/N I (OR SOMETIMES F/N 5.)



SPERRY LINIVAC

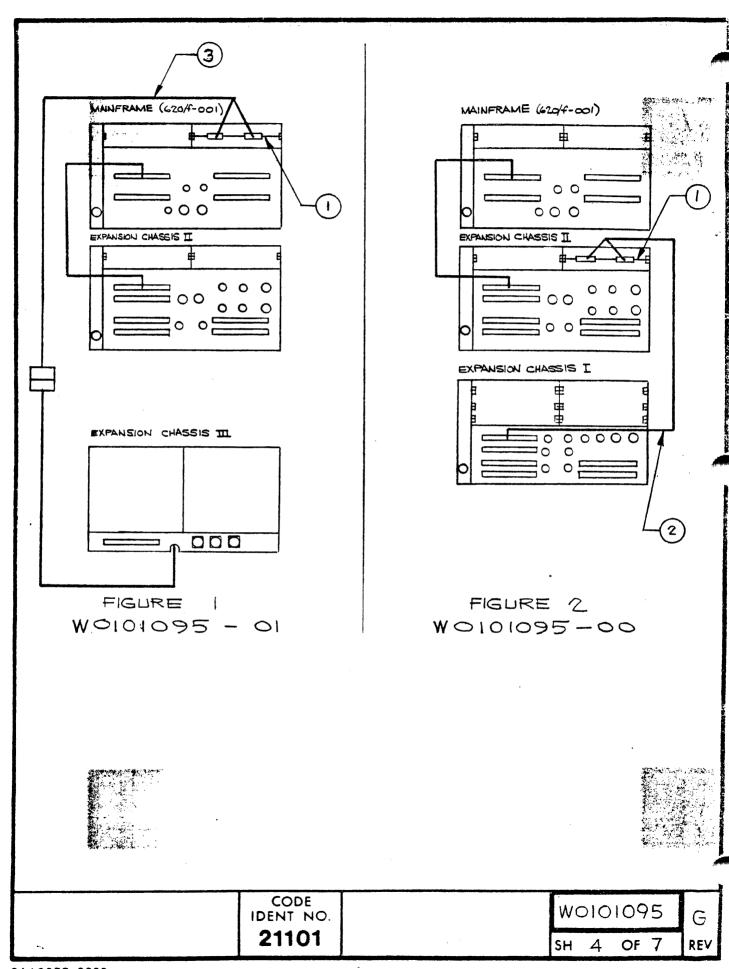
CODE IDENT NO. 21101

W0101095

sh 3 of 7

REV

G



96A0039-000B

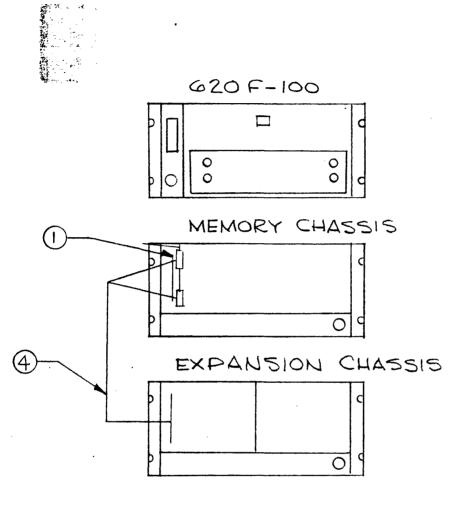


FIGURE 3 W 0101095- 02

NOTE: F/N 5 WILL TYPICALLY BE INSTALLED IN AN END SLOT OF THE EXPANDED" I/O BUS. REFER TO SYSTEMS MEMO FOR DETAIL.

CODE IDENT NO. **21101**

W0101095

G

REV

sh 5 OF 7

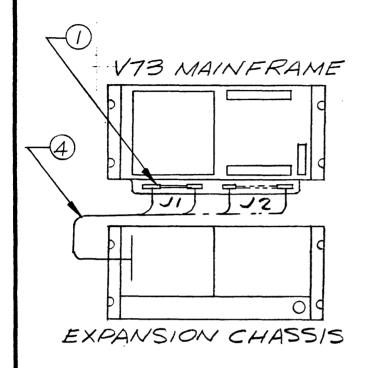
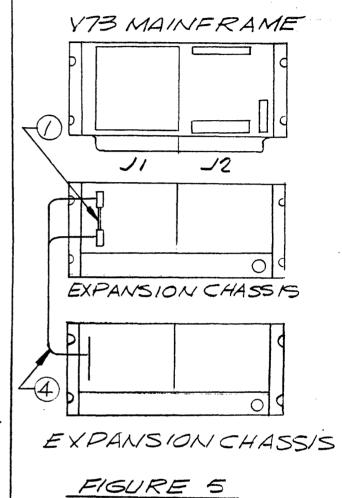


FIGURE 4

NOTE: EOARD MAY BE INSTALLED IN J2 LOCATION.



NOTE: F/N 5 WILL TYPICALLY BE INSTALLED IN AN I/O SLOT AT THE END OF THE EXPANDED BUS.

REFER TO SYSTEMS MEMO FOR DETAIL.

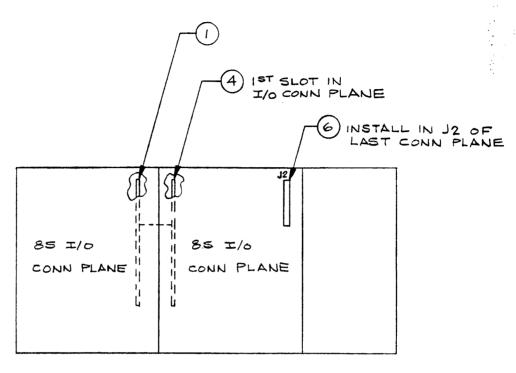
CODE IDENT NO. 21101

w 0101095

SH 6 OF 7

REV

96A0039-000B



V77 I/O EXP CHASSIS

REAR VIEW

FIGURE 6 -03 ONLY

NOTE: IN CERTAIN CONFIGURATION F/N G MAY NOT BE USED; F/N 5 MAY BE SUBSTITUTED. IT WOULD BE INSTALLED IN AN I/O SLOT AT THE END OF THE "EXPANDED" BUS REFER TO SYSTEMS MEMO FOR DETAILS

CODE IDENT NO. 21101

W0101095

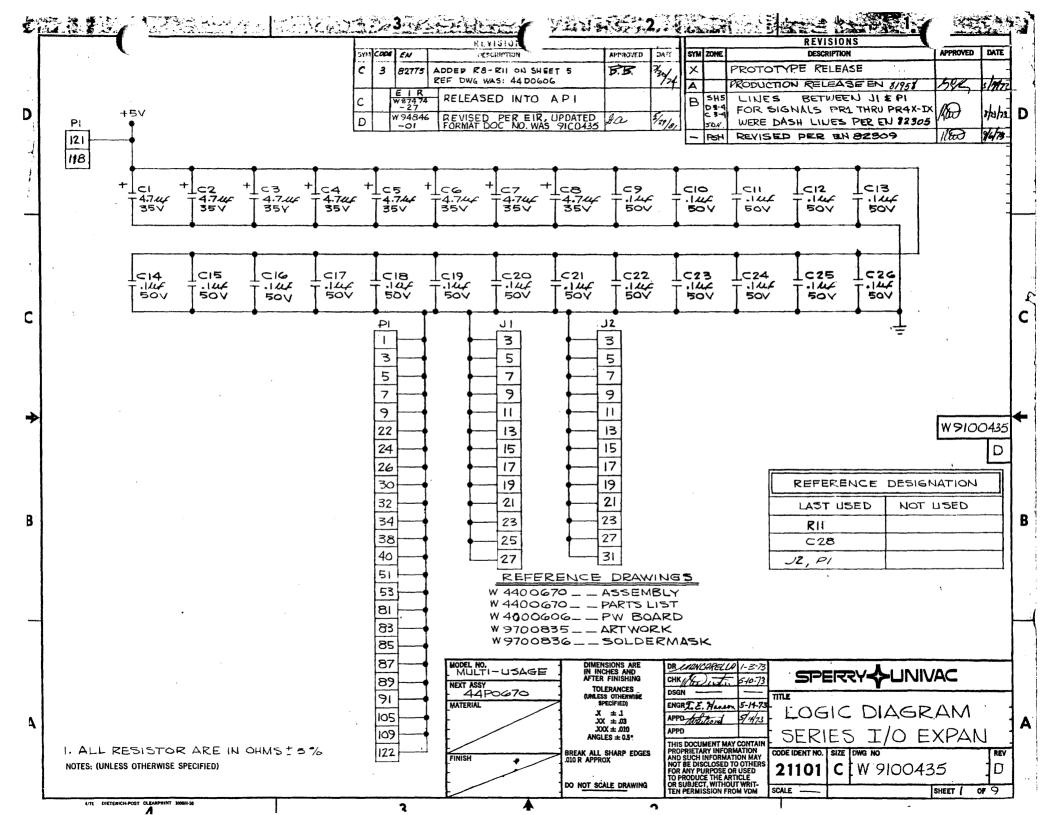
SH 7 OF 7

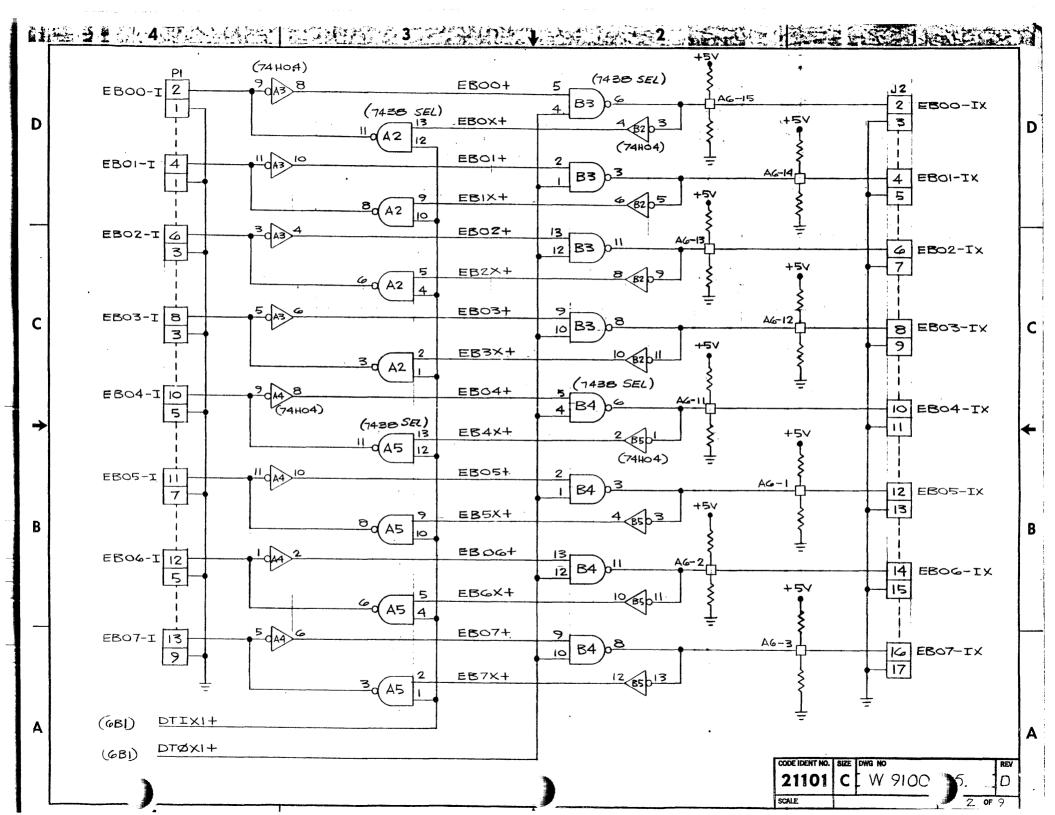
G REV

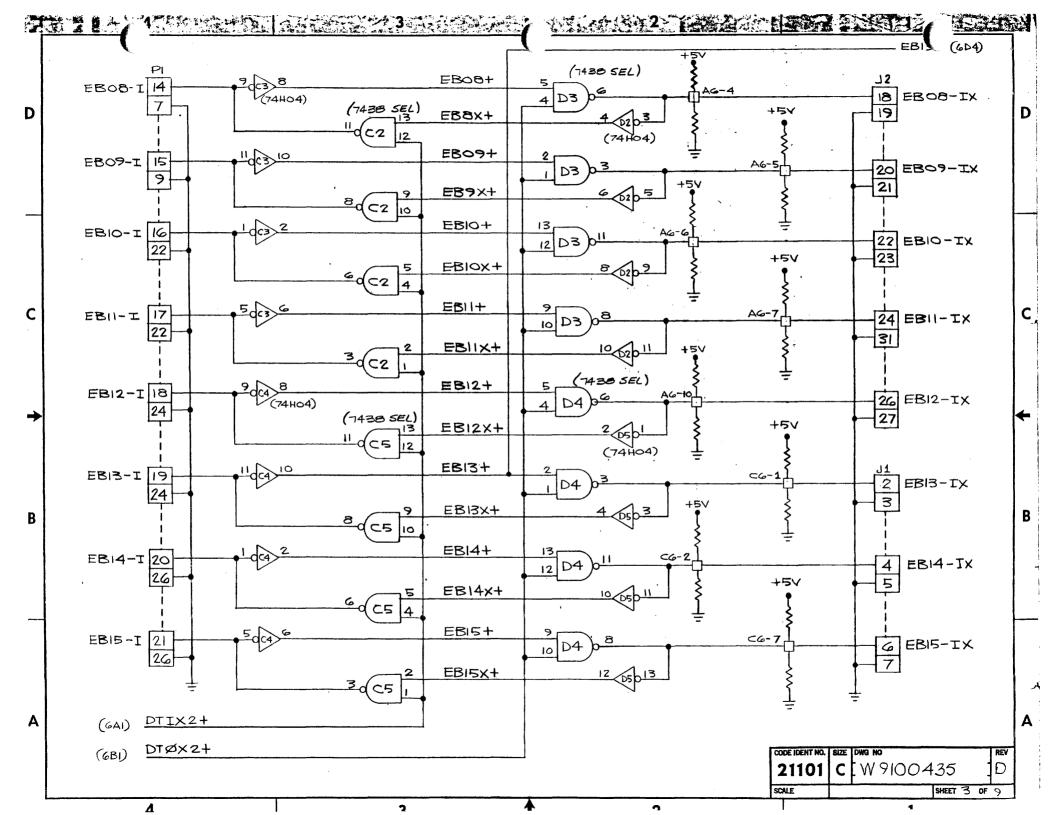
ISSUE DATE CONTROL SHEET DOC NO. LIST PL ₩ 777 81/06/09 W4400670 SPERRY UNIVAC IS A DIVISION OF SPERRY CORPORATION 1 TITLE PCC ADC PCD COMM CODE CLASS U/M ST TYPE A M SIZE PC ASSY SERIES I/O EXPANDER D

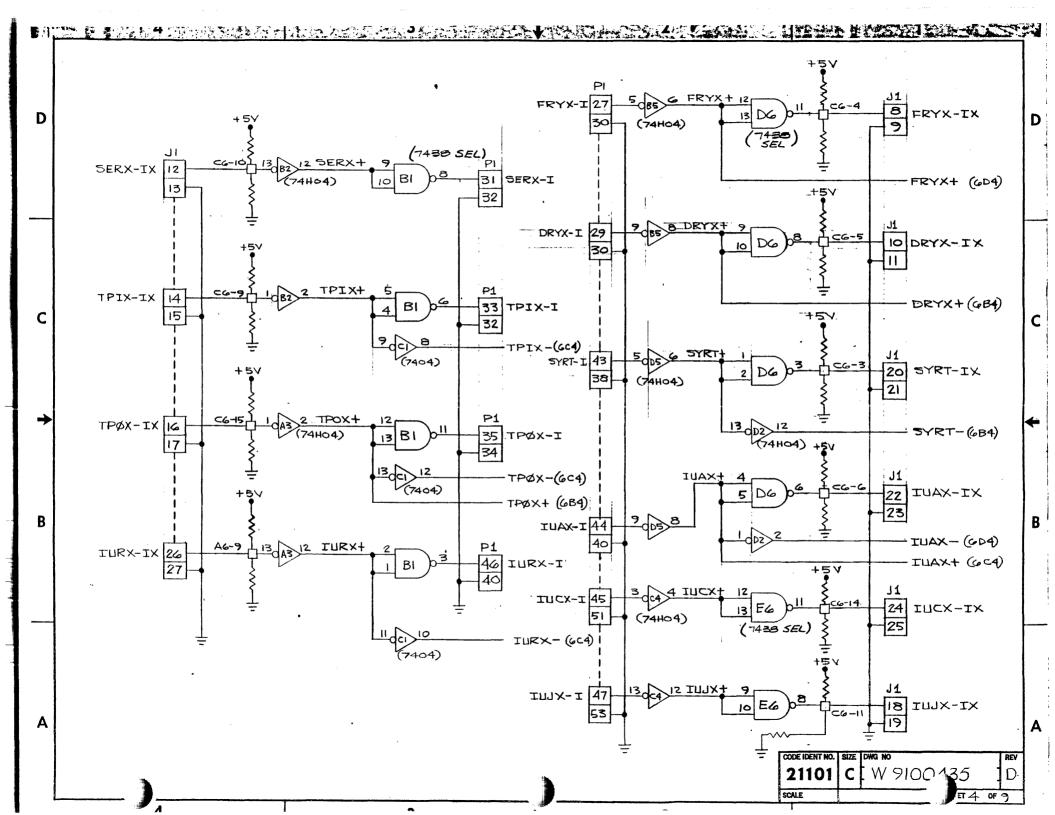
FIND NO.	QUANTITY REQUIRED	U/M F	cc	PART OR IDENT		EIR AND PART DESCRIPTION INFORMATION	ECC	ST C
			1	DOCUMENT NO.	DASH			
2013	1			94846	-01	PL REV N, PIC REV L, RANGE OD - DO EIR RELEASED 81/06/09		:
Z012	1			₩ 946.03	-04	PL REV M, PIC REV L, RANGE DD - DD EIR RELEASED 81/05/20		
****	****	**	*	*****	****	************************ COMMON DATA *******		
1	1	EA		W 4 0 0 0 6 0 6	-00	PC BOARD SERIES I/O EXPANDER DM394		A
2	11	EA		W4900128	-01	INTEGRATED CIRCUIT, DIGITAL TTL 7438 QUAD 2IN NAND		I
3	1	EA	I	30081 83	-00	INTEGRATED CIRCUIT TIL 7404 * GT HEX INVERT		1
4	1	EA	I	30081 86	-00	INTEGRATED CIRCUIT TTL 7410 # GT NAND 3IN		ı
5	. 1	EA		W4900139	-00	INTEGRATED CIRCUIT 14 DIL - PLASTIC OR CERAMIC		1
6	8	EA	1	3007755	-00	INTEGRATED CIRCUIT TILH 74H04 * GT HEX INVERT		1
7	1	EA	I	300 81 94	-00	INTEGRATED CIRCUIT TIL 7474 # FF D DUAL		I
8	1	EA		W49000931	-01	INTEGRATED CIRCUIT, DIGITAL TILH 74H5D		A
9	1	EA	I	5036505	-00	INTEGRATED CIRCUIT TILH 74HO8 # GT AND 2IN		ı
10	2	EA		4916806	-02	RESISTOR NETWORK FIXED 28ELEMT 3 W 2% 200AND 300		A
11	4	EA		W6505000	471	RES,FXD,COMPOSITION .5W 5% 470 OHMS		A
				REF DES	(1)	R1 THRU R4		
12	2	EA		W6502500	752	RES,FXD,COMPOSITION,1/4W,5% 7500 OHMS		A
			į	REF DES	(1)	R5 R7 .		
13	1	EA		W65025 00	102	RES,FXD,COMPOSITION,1/4W,5% 1000 OHMS		A
				REF DES				
14	8 1	EA		W7 1003 50	475	CAPACITOR, FXD, TANTALUM DIEL 4.70 UF		A
			l	REF DES				
15	18	EA		l		CAPACITOR, FIXED, CERAMIC DIEL .1 UF +80%, -20%		
				REF DES				
19	1	EA	1	3007952		INTEGRATED CIRCUIT TTL 74123 * MVB DUAL RGT		
20	2	EA		ı		CAPACITOR, FIXED, MICA DIEL 500V 5% 22 PF		A
			-	į				

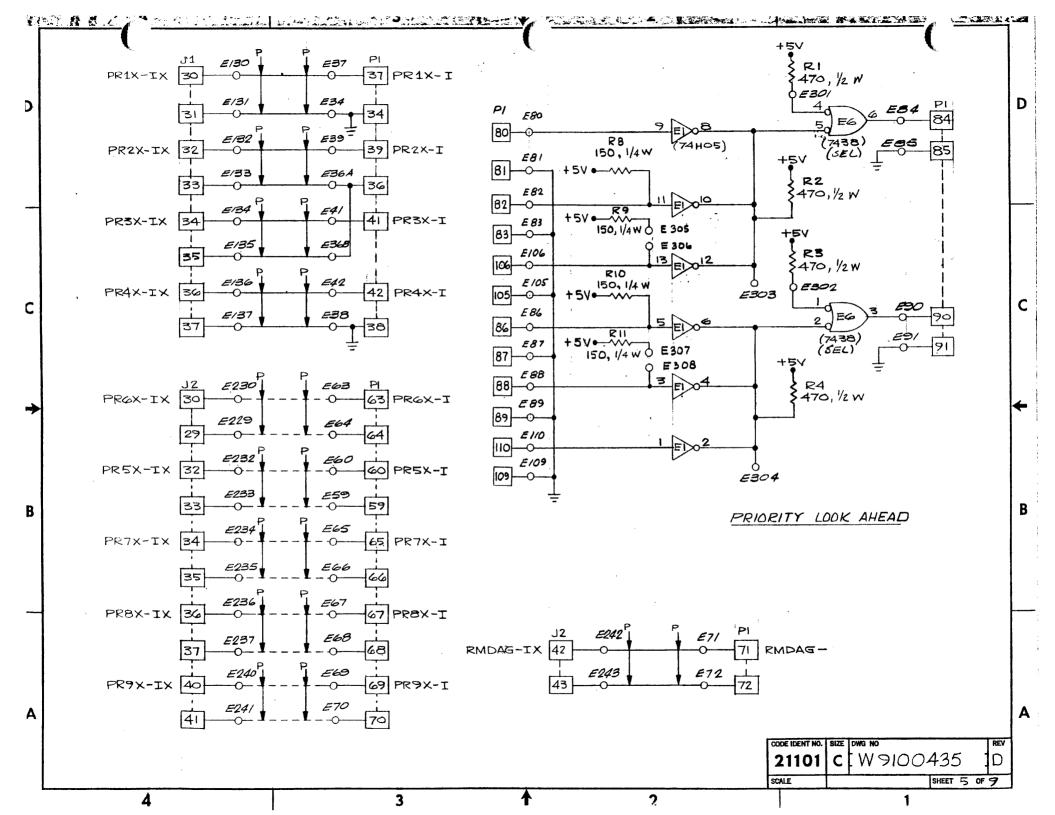
SPERRY JNIVAC PARTS MFG CODE ISSUE DATE CONTROL SHEET DOC NO. J .W 81/06/09 W 777 W4400670 2 SPERRY UNIVAC IS A DIVISION OF SPERRY CORPORATION PCC ADC PCD COMM CODE SIZE CLASS ST TYPE PC ASSY SERIES I/O EXPANDER EA D PART OR IDENT NO. FIND NO. QUANTITY REQUIRED U/M PCC EIR AND PART DESCRIPTION INFORMATION ECC ST CHG DOCUMENT NO. DASH REF DES (1) C27 C28 ARI W5300331 - 05 CABLE, SPECIAL PURPOSE ELEC 21 IN 30 AWG. BLACK & GREEN Α 23 EA RES, FXD, COMPOSITION, 1/4W, 5% W65025001151 150 OHMS A | * REF DES (1) R8 THRU R11 W9100435 -00 LOGIC DIAGRAM F001 X SERIES I/O EXPANDER 5 00 1 MARKING, MECHANICAL SPECS X SW011631-00 | DSGN-F/GENERAL IDENTIFICATION ***** ****** *** ****** VAR DATA PART - 00 ****** A UD1-1517B SHEET 2 OF

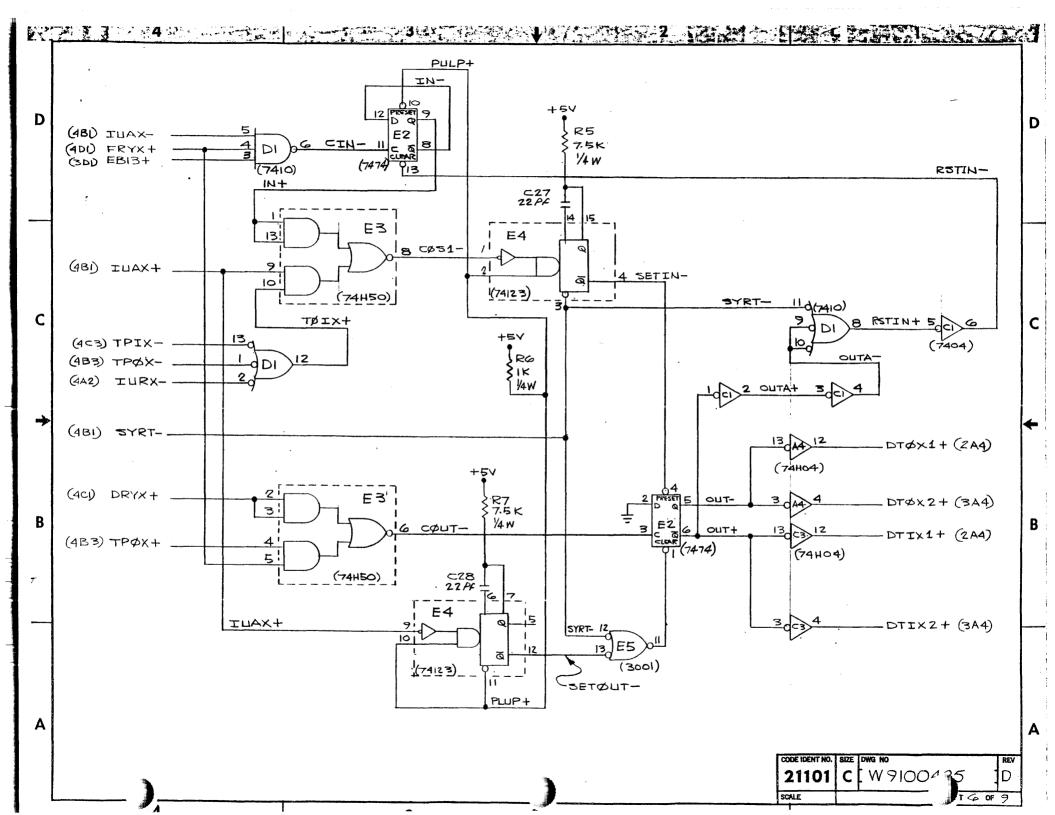


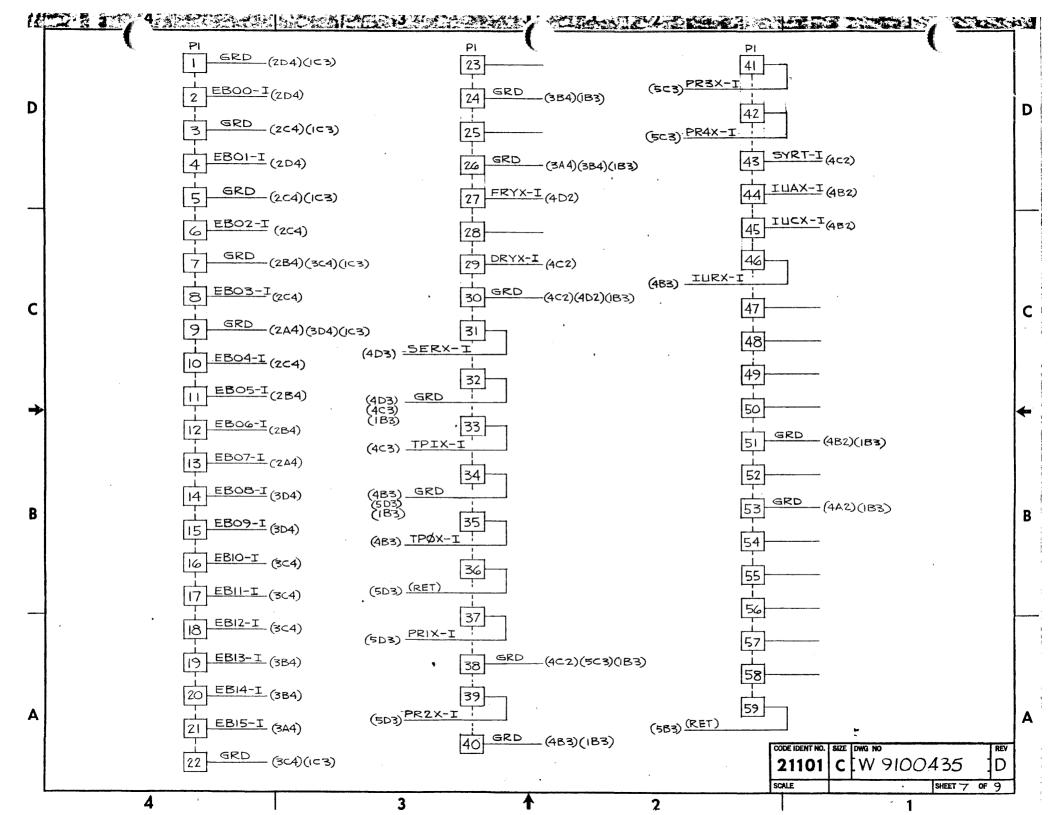


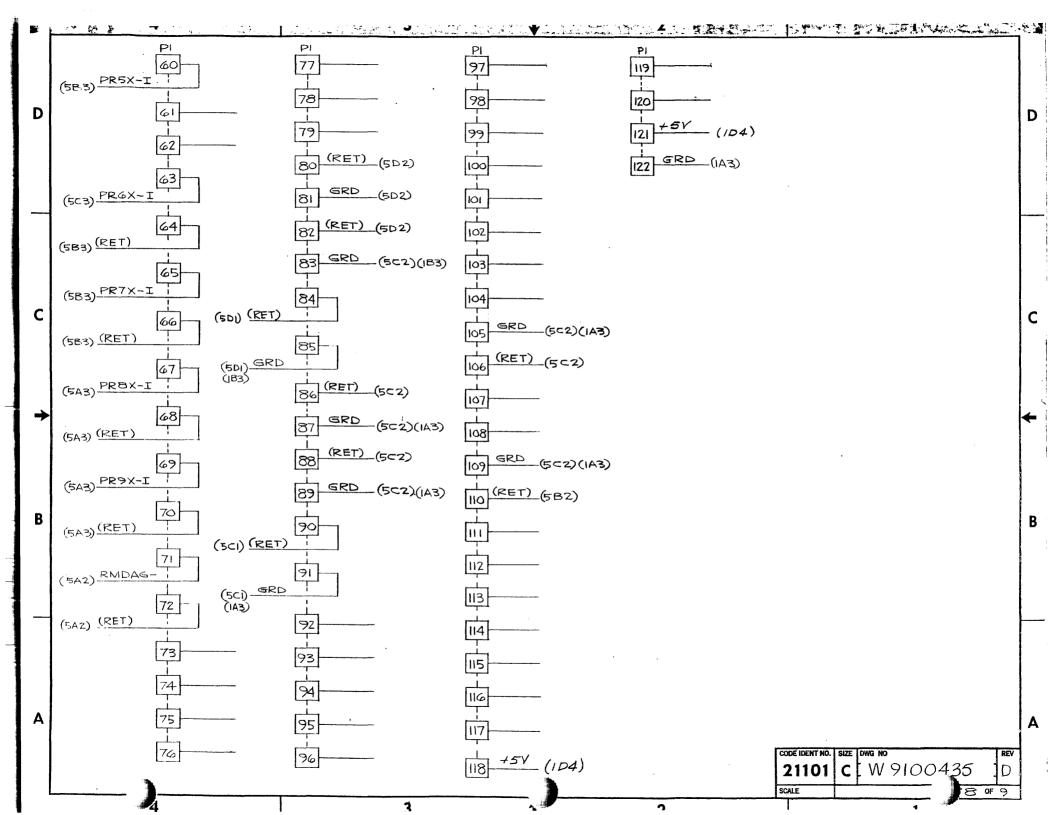


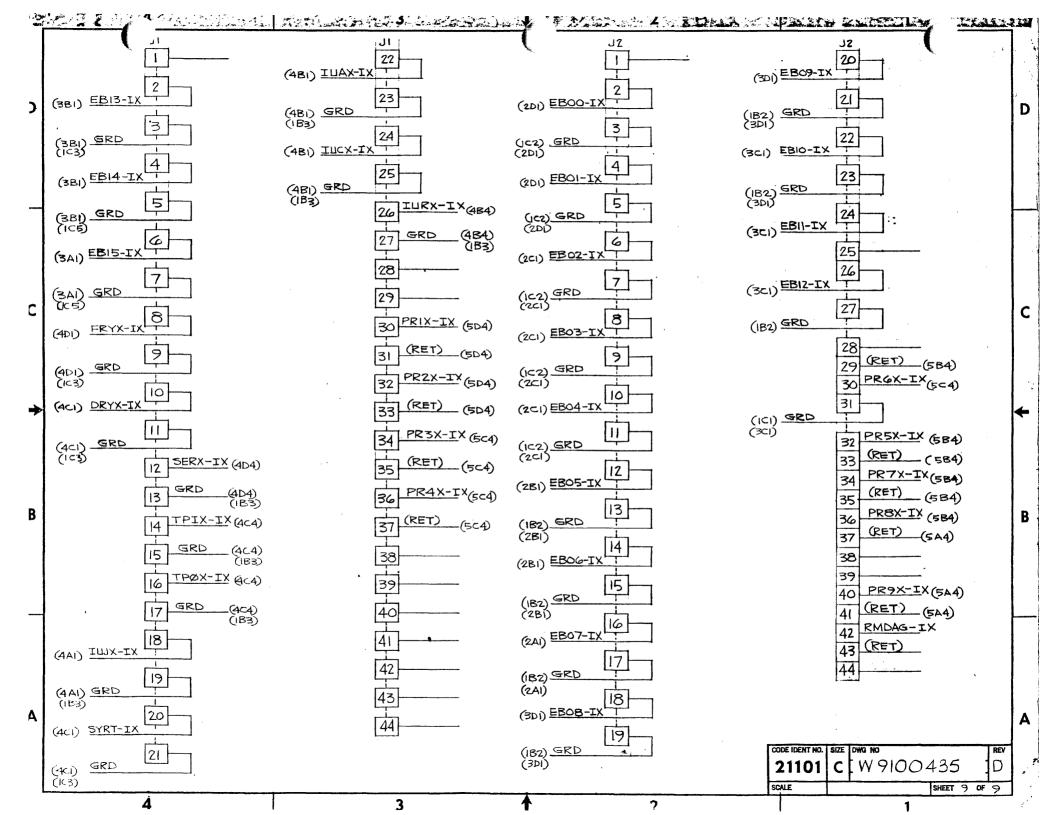








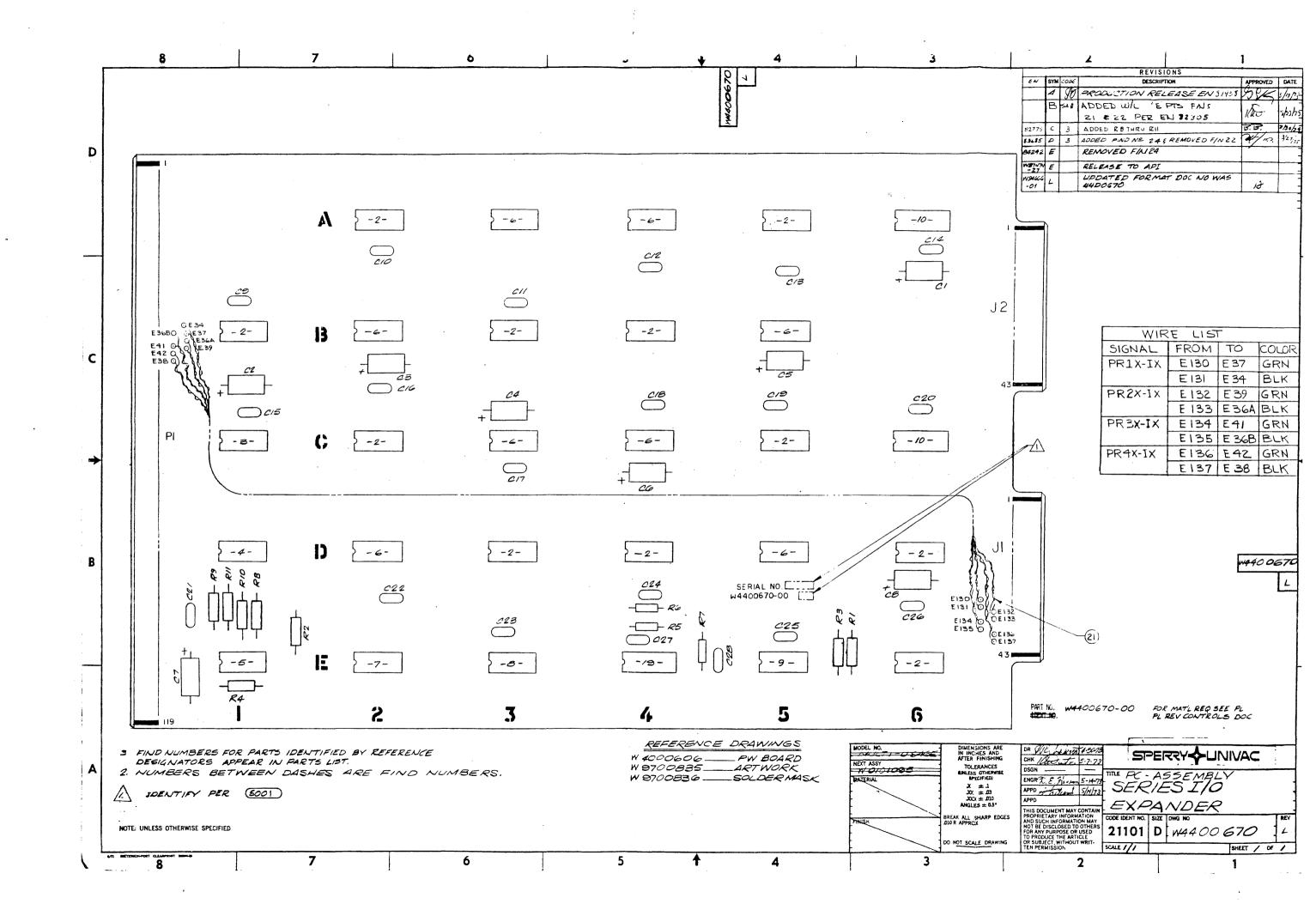


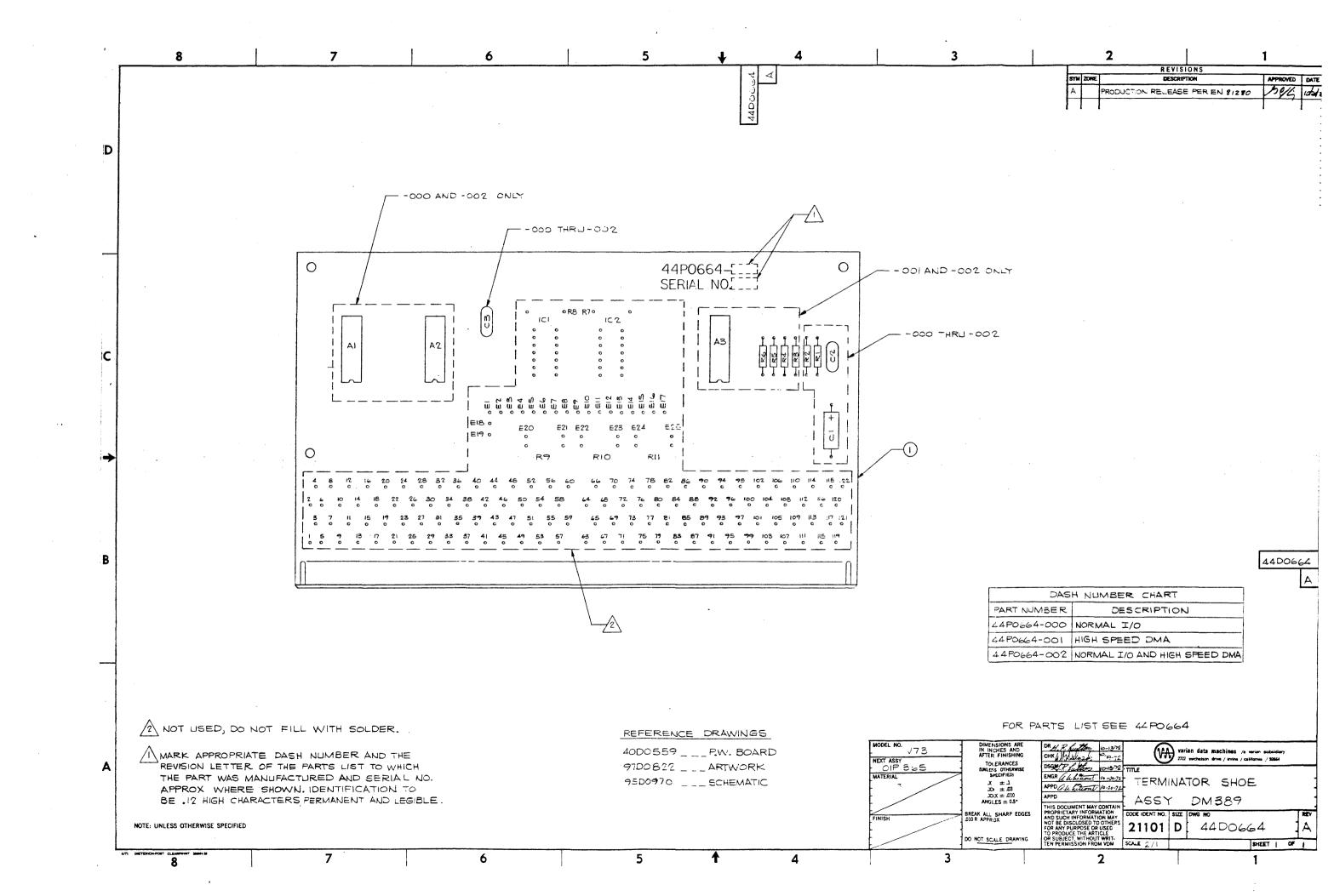


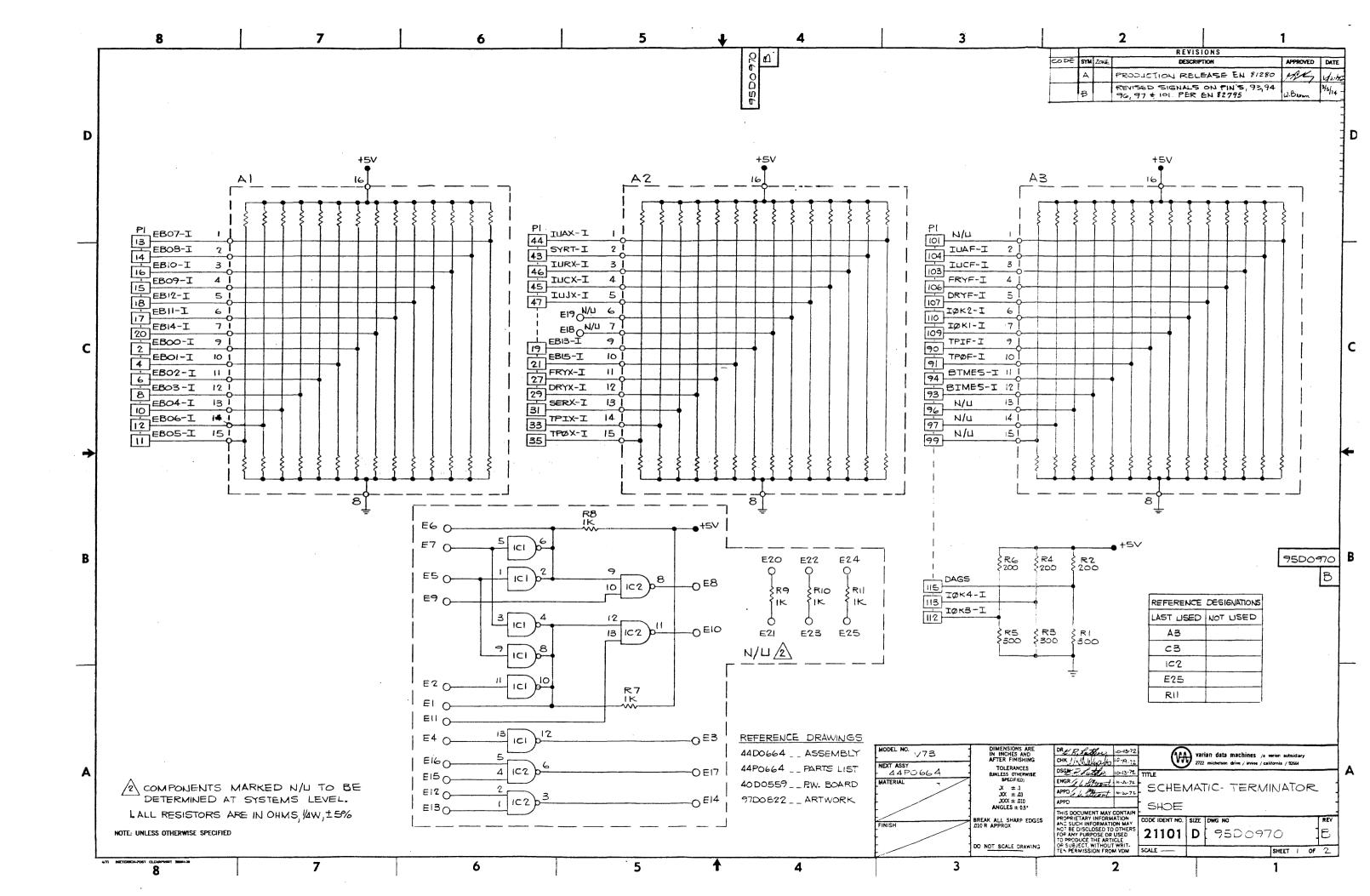
SPERRY	SPERRY UNI			PARTS LIST OF SPERRY BAND CORP.	MFG. CODE W ISSUE DATE CONTROL CA TYPE COMM. CODE ST A PL DOC NO. 4400664 SHEET PL.	REV A	T
PC ASS	SEMBLY -	TERM	SHC	E DM389	CL U/M AC 1 DOC. D RANGE THRU ISSUE PIC.	REV	+
FIND NO.	QUANTITY REQUIRED	U/M	S I Z E	PART OR IDENT, NO. OCUMENT NO. DASH	NOMENCLATURE OR DESCRIPTION	S	
Z01				W-87474 -21	PL REV A, PIC REV A, RANGE OU # 02 EIR RELEASED 10/15/79	,	+
***	******* -	***	***	1	**************************************	*	
1	1;	EA	W	1	PC BOARD - TERM SHUE DM389	1	A
2	1	EA	W	i	CAPACITOR, TANTALUM 35V 10% 2.20 UF	1	A
3	3	EA	lai .	,	C1 CARACTTOR DEPARTS		
	\$' 	FA	"	REF DES	CAPACITOR - CERAMIC .01 UF +80%, -20%	1	A
FO1	! !	X	ial		CZ C3 SCHEMATIC - TERMINATOR SHUE		
501	i I	×		1	HARKING SPEC PART IDENTIFICATION	1	A
***	 * * * * * * * *	***	***	******			A
4	2	EA	W	<u>:</u>	DUAL TERMINATING RES NEIWORK		<u> </u>
	!			i	A1 A2	'	A
5	1	EA	W	6502500 20	RESISTOR, FIXED CUMP, 1/4W, 5%200 UHMS		
i	, 			i	R2	'	1
6	1	EA	W	6502500 30	RESISTOR, FIXED CUMP, 1/4W, 5%300 UHMS	١,	A
	 			l l	RI		
***	*****	***	***	***	*HIGH SPEED DMA * VARIABLE DATA ** 01*********	麻 奔 i	*
4	11	EA	W	4800007 -00	DUAL TERMINATING RES NETWORK	1	A
	 			REF DES	A 3		
5	3	EA	W	i i	RESISTOR, FIXED COMp, 1/4W, 5%200 OHMS	t	A
	 			4	R2 R4 R6		
6	3	EA	W	1	RESISTOR, FIXED COMP, 1/4W, 5%300 OHMS	1	A
	i			· •	R1 R3 R5		
****	******** _!	***	***		*NORMAL I/O AND HIGH SPEED DMA + VARIABLE DATA - 02**********	# # 1	*
4	31	EA	W	:	DUAL TERMINATING RES NETWORK	1	A
!	; 			REF DES	A1 A2 A3 .		
UD1-1517 RE	FV PRIM	NTEO N. S. A.				丄	\perp

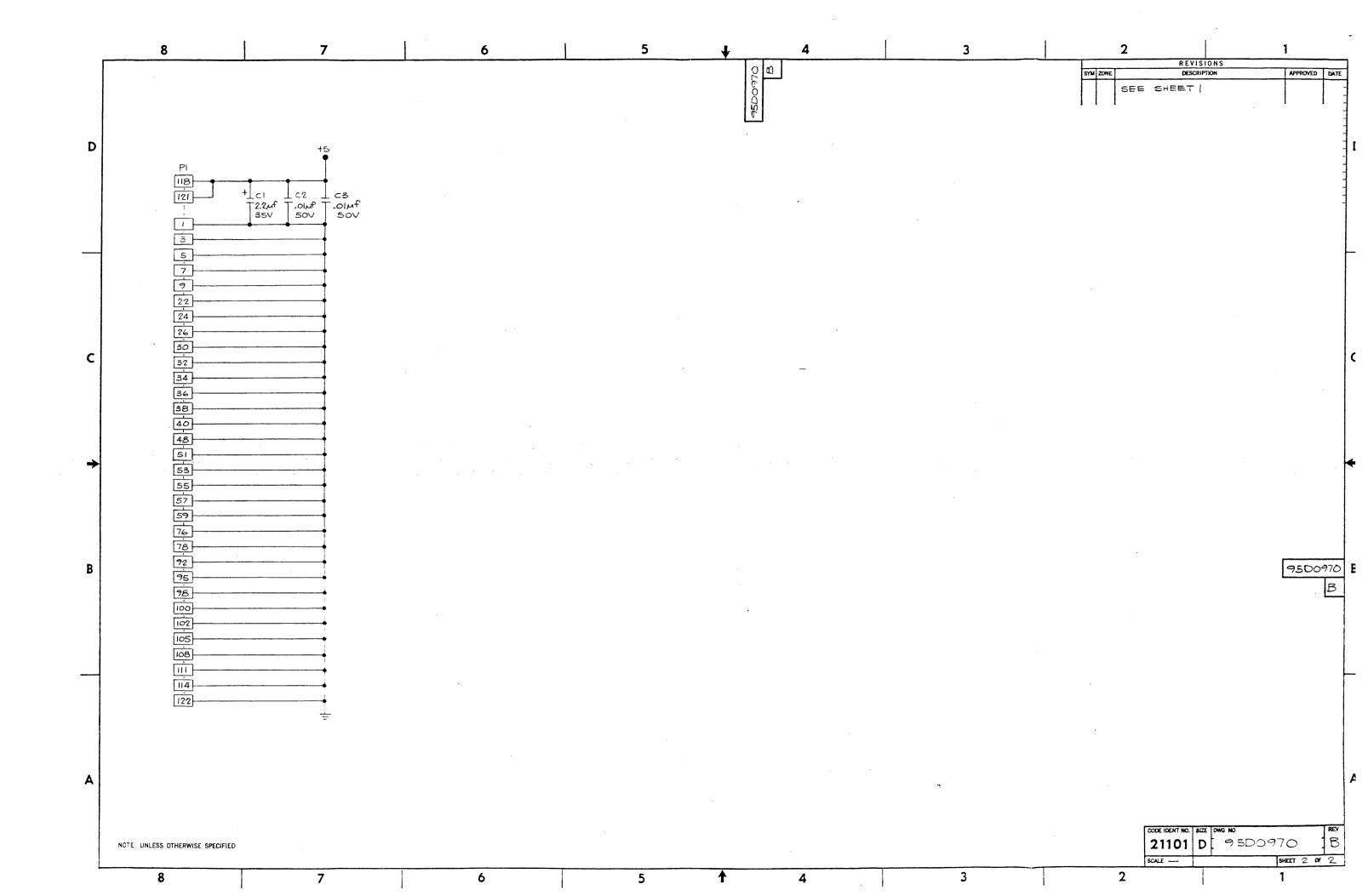
		VIVAC IS	SAD	IVISION	PARTS N OF SPERRY R	AND CORP.	MFG		W 10/1	5 X W	777	YPE COMM.	CODE	A PL	DOC. NO.	4400664	S	PL. REV.	
C AS	SEMBLY .	TER	₹M	SHU	E DM38	⇒ '					CL U	EA	A	1 DOC D	RANGE	THRU	ISSUE	PIC. REV.	
ID NO.	QUANTITY REQUIRED	U/M	S		PART OR IDEN		_	· · · · · · · · · · · · · · · · · · ·			NOME	NCLATURE O	D DECCRIPT	011				S	; (
5	3	E	E		650250			e t n p	ETVEN	COM-								S P	5 1
	!	=,	•	"							1744,	22500	UHMS	1				Α,	4 7
	_!				REF DE		1												1
6	3	E	A	W	65025	0 301	RESI	STOR,	FIXED	COMp,	1/4W,	5%300	OHMS					4	A :
	!				REF DE	. S 1	R1	R3	R!	5									١,
	† †					į													
	1																		
1	! 					1													
	 					į													
						i I													
	 					İ													
						į													
	į					į													
	!																		
	!					l l													
	1					1													
	!					İ													
	į					į													
						-	İ												
						1													ļ
	1					l													
	!				•	1													
	1					1													
	į					į													l
1	į					į											•		
						ļ													
	 					1													
	 		l			†													
	İ					İ													
	į					į													
	1	1	1	i		1	1											ſ	

•









57EIZ	SPERF			A DIV	PARTS LIS	W 10/22/79 W777 M M PL W 0101893 1 6	REV.
I'50 E					S OPTION	CL U/M AC DOC. A RANGE THRU ISSUE PICE	REV.
FIND NO.	QUANTI REQUIRE	TY D	U/M	Š I Z	PART OR IDENT. NO.	NOMENCLATURE OR DESCRIPTION	S
205				Ε.	W-87830 -	8 PL REV E, PIC REV C, RANGE 00 - 03 EIR RELEASED 10/09/79	
***** 7	****	***; 1	EA	* * *	********* W 5300686!=	**************************************	A
01			X		,	O INSTALLATION DRAWING	A
01			X			0 MARKING SPEC PART IDENTIFICATION	A
**	*****	***	***	* * *	*****	**1ST CHASSIS RH BACKPLANES # VARIABLE DATA # 0.0***********	4 [
i		1				ABOVE PT NO INACTIVE FOR NEW DESIGN 10/09/79 W-87830+08	
1		1	EA		W 0101267 -	1 EXP. CHASSIS ASSEMBLY - I/O L.H. I/O R.H. I/O	A
2		1	EA			2 INSTL KIT, CHASSIS ASSEMBLY	A
4		1	EA			4 CABLE ASSY I/O EXP RIGHT HAND	A
***	*****	***	***	**	******	** 2ND & SUB CHASSIS LH RH BACKP * VARIABLE DATA - 01**********	* 1
į						ABOVE PT NO INACTIVE FOR NEW DESIGN 10/09/79 W-87830-08	
1		1	EA		W 0101267 -	1 EXP. CHASSIS ASSEMBLY - I/O L.H. I/O R.H. I/O	A
3		1	EA		W 0101081 -	3 INSTL KIT, CHASSIS ASSEMBLY	A
Sį		1	EA		W 5300547 +	O CABLE ASSY - EXPANSION	A
6	,	4	IN		W 5300035 2	6 CABLE, JUMPER	A
***	*****	* * *	***	* * *	•	* 1ST CHASSIS LH RH BACKPLANES + VARIABLE DATA - 02**********	* *
5		1	EA			2 INSTL KIT, CHASSIS ASSEMBLY	A
4		1	EA		4	4 CABLE ASSY I/O EXP RIGHT HAND	A
8		1	EA			4 EXP. CHASSIS ASSEMBLY - I/O L.H. I/O R.H. I/O	A
***	*****	***	***	**		** END & SUB CHASSIS LH RH BACKP * VARIABLE DATA ** Q3*************	*
3		1	EA			3 INSTL KIT, CHASSIS ASSEMBLY	A
5		1 i	EA			O CABLE ASSY - EXPANSION	A
6 8		41	IN			6 CABLE, JUMPER	A
O,	1	4	EA		W 0101267! -	4 EXP. CHASSIS ASSEMBLY - I/O L.H. I/O R.H. I/O	A

			revisions		
REV	EN	CHG CODE	DESCRIPTIONS	DR	APPD
С	87086		REV MODEL NO. BLOCK, REV TABULATION, ADDED NOTE A REV DWG TO CONFORM TO 01P1893.	KIVI	Aruna 8/24/8
C ₁	87363		TABULATION: MODEL NO. 70-9010 P/N WAS: 01P1893-003, MODEL NO. 70-9011 P/N WAS: 01P1893-004		14MX

		TABULATION
PART NO.	MODEL NO.	DESCRIPTION
01P1893-000	70-9010 Q	1st CHASSIS RH BACKPLANES.
01P1893-001	70-9011 Q	2nd AND SUB CHASSIS LH AND RH BACKPLANES.
01P1893-002	70-9010	1st CHASSIS LH AND RH BACKPLANES.
01P1893-003	70-9011	2nd AND SUB CHASSIS LH AND RH BACKPLANES.
	01P1893-000 01P1893-001 01P1893-002	01P1893-000 70-9010 Q 01P1893-001 70-9011 Q 01P1893-002 70-9010

FOR PARTS LIST SEE 01P1893

	ASSEMBLY ND ITEM		MODEL NO. SEE TABUL	ATION	UNIVAC						
DR	S. JURISCH	1/15/76	CODE	21101	TITLE						
СНК	BROWN FIELD	3/9/76	IDENT NO.			I/O EXPANSION CHASSIS OPTION					
nsgn			THIS DOCUMENT I PROPRIETARY IN SUCH INFORMATI	FORMATION AND		OPTION					
ENGR	J. JENNINGS	3/13/76	DISCLOSED TO OT	THERS FOR ANY	SIZE	DWG NO.	REV				
APPD		3/13/16		SUBJECT, WITH-	A	01A 1893	C				
APPD			UNIVAC .		SHEET 1 OF 4						

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. This drawing provides for the I/O Chassis Option for the V76.
- 2. For installation into a Rack, see Installation Drawing 93E0406.
- 3. Identify per Specification 98A1163.



Locations shown are for reference only. Actual locations to be determined by Systems Engineering.



Term Shoe supplied with Mainframe.

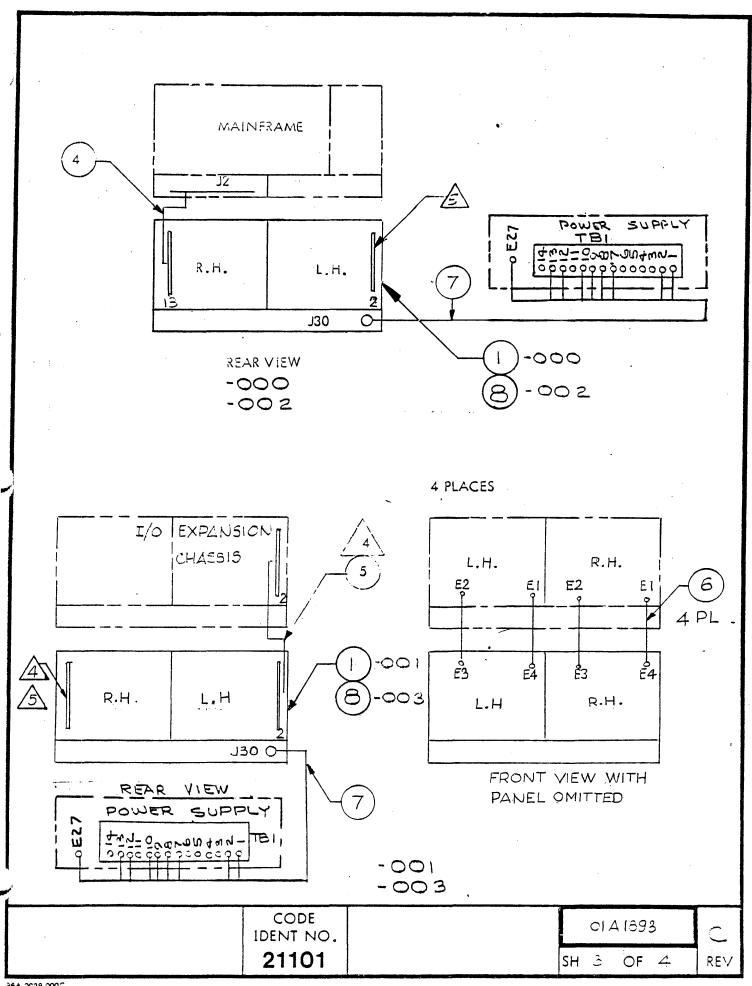


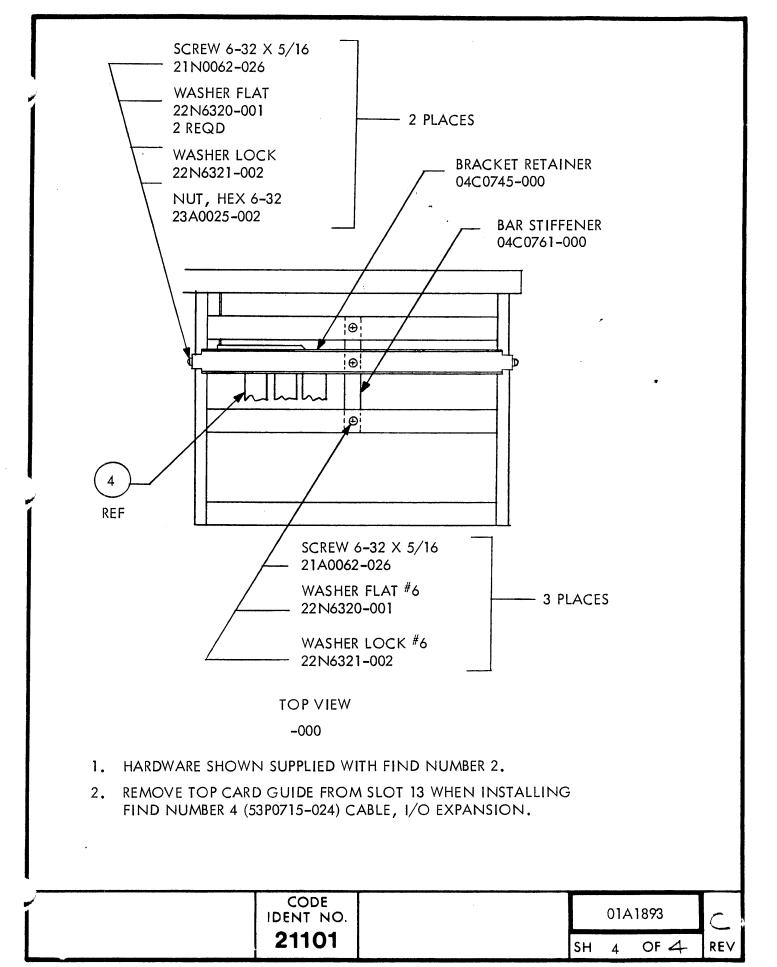
Inactive For New Design.

CODE IDENT NO. **21101**

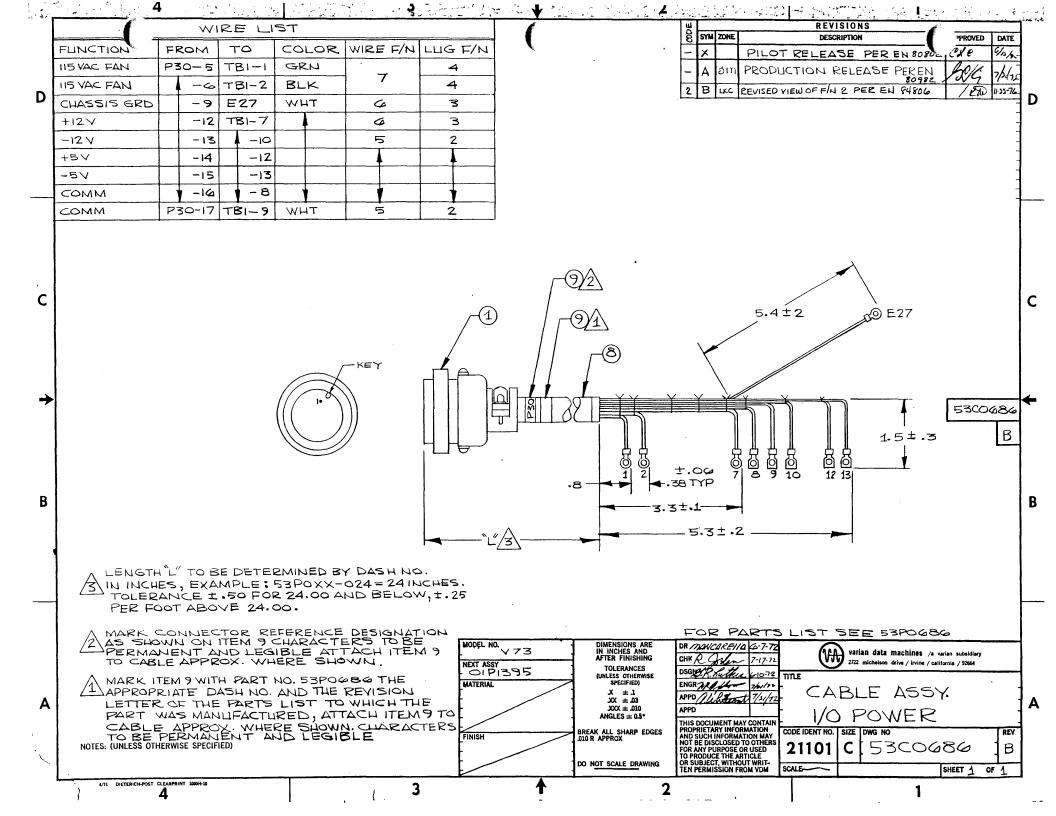
01A1893

SH 2 OF 4





SIPER?	Y \$ UNIVA		-	PARTS LI		MFG CODE ISSUE DATE CONTROL CA TYPE COMM CODE ST PL W 5500680 1 C	i 7-	
CABLE	ASSY .			OF SPERMI NAME	COM	CL U/M AC Doct KANSE from 1550s PIC F	RE V	
ON CHI	QUANTITY REQUIRED	U/M	, ,	FA-T IR ; [21] NO	DASH	NOMENLATURE OR DESCRIPTION	5	Ī
203	****		* * * *	×-87849		PL REV C, PIC REV 3, RANGE DO 4998 EIR RELEASED 10/24/79		
1	1	EA	W	5700118 REF DES		CONNECTOR PLUG NON LONG LOCKING RING P30	A	
5	5	EA	M	5800158	-00	TERMINAL LUG, INSULATED 12-10 AND NO.6 STUD	A	
3	2	EA				TERMINAL, RING TONGUE, INS #6 16-14 AWG	A	
R	5	EA				TERMINAL, RING TONGUE #22-16 AWG WIRE - #6 STUD	A	
5	ARI	IN	1	i		WIRE STRANDED, I.P.V.C. 10 ANG WHITE	A	
7	AR;	IN		•		WIRE STRANDED, I.P.V.C. 16 AND WHITE WIRE, STR. INISTED CAIP, I. V.C. 18 AND BLACK & GREEN	A	
8	AR	IN				TUBING, INSULATED, NON-SHRINK SELECTION TO BE HADE	Δ	1
9	2	EA				CLAMP-HARNESS, IDENTIFICATION	i	
301	!	x				MAPKING SPEC PART IDENTIFICATION	A	
	1							
	i !			1				
	i -1			į				
i	i							
	!			ļ				
				į				
	1			į	•			
	i !							
				i				
UD1-1517 R	EV 275	<u> </u>						
301-1317 #		7.	DUP L 1	CATE		SHEET 1 OF 1		



SPEI	RZY:				PARTS L		MFG. CODE ISSUE DATE CONTROL CA TYPE COMM. CODE ST. DOC. NO 5300715	EET PL RI	. 1
CABL	.E /				HT HAND	CORP.		SUE PIC. R	REV.
FIND N		QUANTITY REQUIRED	U/M	S Z E	PART OR IDENT. NOCUMENT NO.	DASH	NOMENCLATURE OR DESCRIPTION PL REV D, PIC REV B, RANGE 00 -998 EIR RELEASED 11/1	6/79	S C P G
Z03	!	*****	****	***	NONE:	1	PL REV C, PIC REV B, RANGE 00 #998	***	
1	i I	1	EA	W		:	PC BOARD (DM310) PC BOARD (DM369)		A
3		6	EA	W	5700259	-01	CONNECTOR, FLAT CABLE BODY AND COVER		A
5		AR AR	IN	M		I	CABLE, FLAT 40 WIRES 65 OHMS IMP WIRE BUS 22 AWG		A *
6	: 1	AR AR	IN	M		•	INSULATION SLEEVING ELEC SIZE 22 BLACK .028 ID		A
8		3	EA	W		i	TAPE, ADHESIVE CLAMP HARNESS = WEDGE LOCK 7.31 x .184		A
9 301		1	EA	W	0400757 SW01163	l	BRACKET PART IDENTIFICATION MARKING SPEC		A
\$02		 	X			:	SELECTION & INSTALLATION SPEC MACH SCREWS & ASSOC HARDWARE.		A
	 	! ! !				1			
		. !				 			
		1				! ! !			
		! ! !				 			
						† † 			
UD1-1	517 RFV	V. 2-75	SINTEO IN U.S.A.	DUPL	ICATE		SHEET 1 OF)	

